

Amendments to the Claims

1. (CURRENTLY AMENDED) Integrated circuit (~~IC~~) comprising a network and a plurality of modules (~~M₁, M₂, M₃ up to and including M_n~~), which are arranged to communicate to each other via the network, wherein the network is arranged to establish transactions between a first module (~~M₁~~) and at least two second modules (~~M₂, M₃ up to and including M_n~~), characterized in that the network is arranged to replicate a single request (~~SREQ~~) from the first module (~~M₁~~) into at least two replicated requests, and that the network is arranged to send the replicated requests to the second modules (~~M₂, M₃ up to and including M_n~~).
2. (CURRENTLY AMENDED) Integrated circuit (~~IC~~) according to claim 1, wherein the network comprises an address space (~~ADDR_SPC~~) and a facility for mapping at least one multicast address (~~F000~~) onto at least two further addresses in a range of addresses (~~0 up to and including EFFF~~).
3. (CURRENTLY AMENDED) Integrated circuit (~~IC~~) according to claim 2, wherein the network further comprises a facility for mapping at least one first multicast address onto at least one second multicast address, provided that the second multicast address is not mapped onto the first multicast address.
4. (CURRENTLY AMENDED) Integrated circuit (~~IC~~) according to claim 2, wherein the network further comprises a facility for mapping a range of multicast addresses (~~F000+x~~) onto at least two ranges (~~1000+x, 3000+x~~) of further addresses.
5. (CURRENTLY AMENDED) Integrated circuit (~~IC~~) according to claim 1, wherein the single request (~~SREQ~~) comprises a connection identifier (~~CID~~) which identifies a multicast connection.
6. (CURRENTLY AMENDED) Integrated circuit (~~IC~~) according to claim 1, wherein a network interface (~~NI~~) is arranged to perform the replication of the single request (~~SREQ~~) into the replicated requests, and wherein the network interface is

arranged to send the replicated requests to the second modules (~~M₂, M₃ up to and including M_n~~).

7. (CURRENTLY AMENDED) Method for sending requests in an integrated circuit (~~IC~~) comprising a network and a plurality of modules (~~M₁, M₂, M₃ up to and including M_n~~), which communicate to each other via the network, wherein the network establishes transactions between a first module (~~M₁~~) and at least two second modules (~~M₂, M₃ up to and including M_n~~), characterized in that the network replicates a single request (~~SREQ~~) from the first module (~~M₁~~) into at least two replicated requests, and that the network sends the replicated requests to the second modules (~~M₂, M₃ up to and including M_n~~).